

AMENDMENTS TO THE SPECIFICATION

Please amend paragraph 199 of the Specification as follows:

[0199] Next, FIG. 20B shows a relation between the layout of the information area of the optical disk 15 and its logical address in a state where the optical disk 15 is ejected after interrupting the BGF process. This example shows a state of obtaining logical compatibility with a single sided dual layer DVD-ROM by recording dummy data in the unrecorded area following the corresponding position in the recording layer M1 and recording data in the temporary intermediate area and the lead-out area. Same as the example shown in FIG. 20A, the start position of the data area in the recording layer M0 is deemed as logical address of "000000h". The ~~physical~~ logical address increases continuously in the data area in the recording layer M0 toward the outer periphery of the optical disk 15. In a case where the physical address of the start position in the temporary intermediate area is N (<M), the logical address of the end position of the data area in the recording layer M0 becomes (N-1)-30000h, and the logical address of the start position of the data area in the recording layer M1 becomes N-30000h. Then, the logical address increases continuously from the start position of the data area in the recording layer M1 toward the inner periphery of the optical disk 15.
